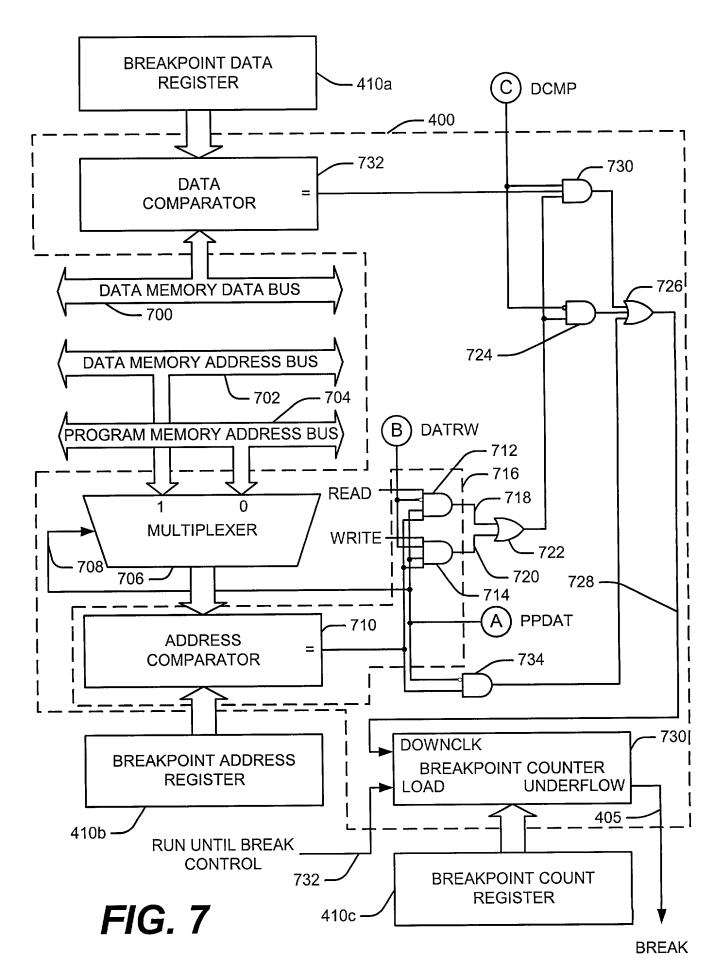


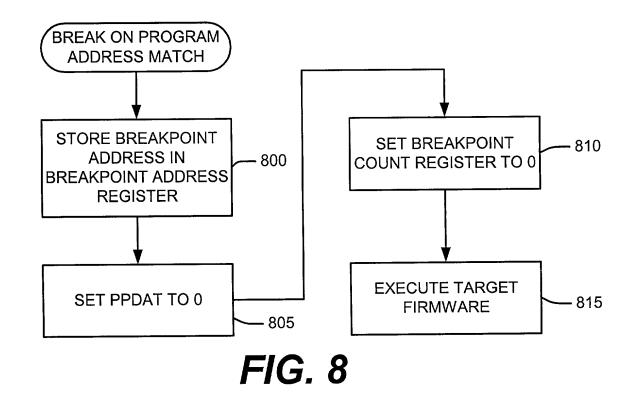
FIG. 5

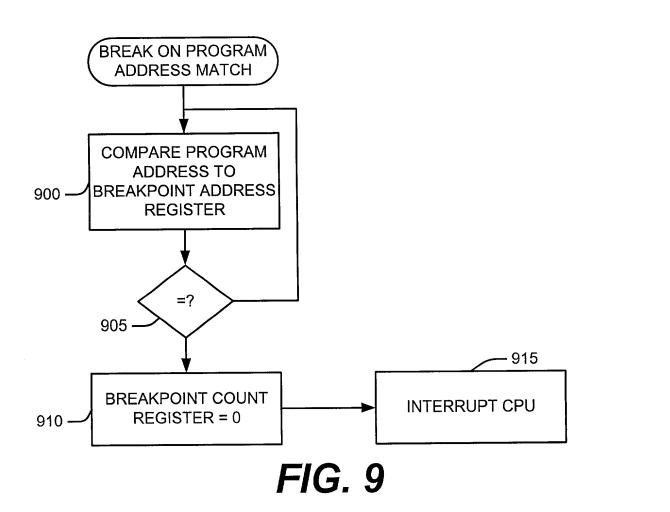
DEBUG REGISTER

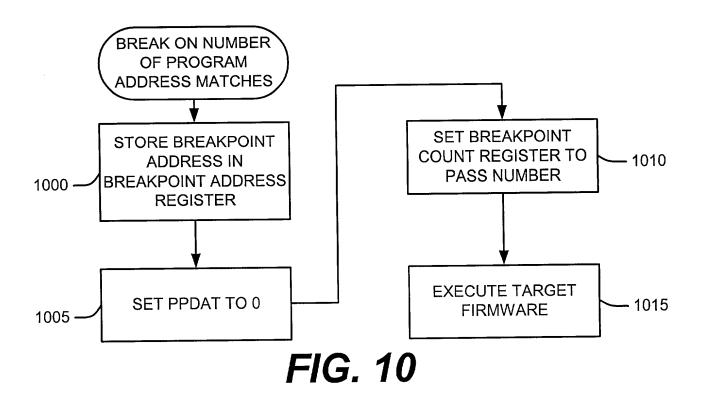
DEBUG REGISTER									
600									
600									
COE									
- 605									
BIGBUG REGISTER									
610									
- 615									
0.0									
BITS 7-0 OF BREAKPOINT ADDRESS REGISTER BITS 7-0 OF BREAKPOINT DATA REGISTER									
- 620									
- 020									
_									

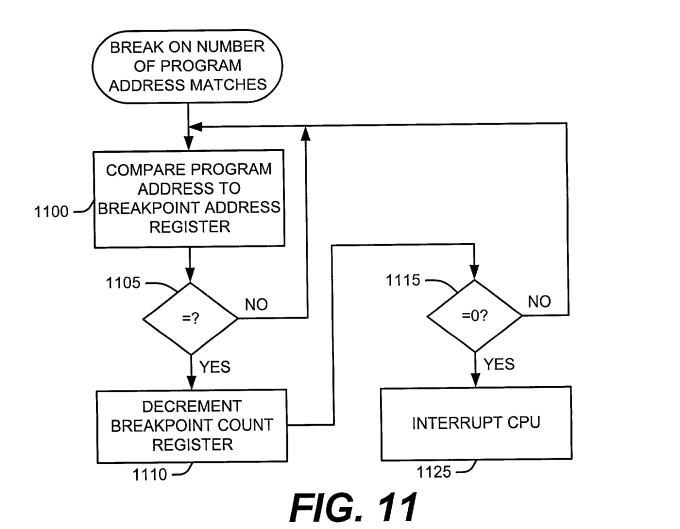
FIG. 6











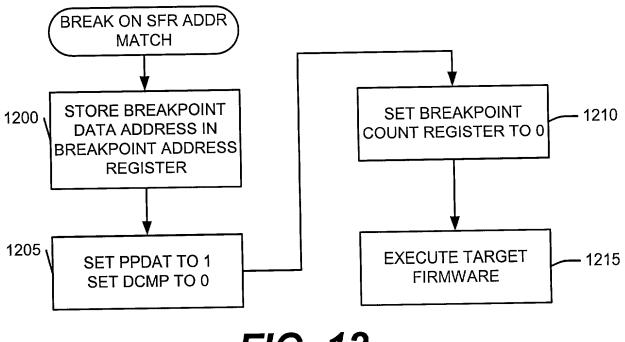


FIG. 12

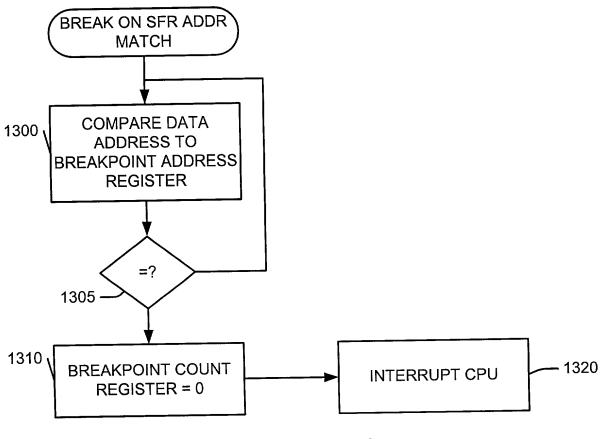


FIG. 13

